

IMAGER WITH ADJUSTABLE RESOLUTION

ABSTRACT OF THE DISCLOSURE

A photodetector array includes a plurality of photodetectors, preferably photodiodes, coupled to a respective plurality of addressable interface circuits. At each pixel, a switching circuit configures neighboring ones of the photodetectors into pixels by summing multiple photodetector signals into an aggregated pixel output signal. The switching circuit is electronically switchable to aggregate said photodetector signals according to at least two different selectable pixellization schemes with differing resolution.